### An Assembler for the Tiny Computer

Chuck Thacker, MSR

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### Introduction

The program TCasm.exe is an extremely simple assembler for the Tiny Computer. To invoke it from a shell, type: “TCasm source memName”. memName is the names given to the three output files produced by the assembler. It is postpended with 0 for the instruction memory, 1 for the data memory, and 2 for the register file. Although it is possible to specify the contents of all three memories this way, only the instruction memory file (memName0.coe) is important.

After assembly, the program is “loaded” by regenerating the instruction memory core in the design using the ISE. This preloads the bitstream file with the contents of the instruction memory. The register file and the data memory are initialized to zeros when the FPGA is configured.

### The assembler

The assembler is very simple, doing most of its work by looking up textual tokens from the source in a symbol table, and emitting the instruction fragment into the current instruction. The symbol table is populated with definition directives placed at the start of the source file, and by symbols representing labeled statements when these are encountered during assembly. An instruction must occupy a single line of source text, and must be terminated with “;”. Any remaining text on the line is ignored. Case is significant. Numbers may be decimal or hex (0xnnn). Tokens are separated by white space.

The assembler is two-pass. During the first pass, the assembler skips tokens that are undefined. Isolated strings that resolve to fields are placed in currentValue with the indicated offset. Isolated numbers are also placed in currentValue, using an offset of 3.

Symbol table entries have a *key* and *contents* which is a *field* containing a *value* and an *offset* in the instruction (the bit position into which the value should be placed). Other built-in reserved words are treated as follows:

“field *string* *value* *offset*” declares a symbol. The next token must be a string and the next two must be numbers (or names that resolve to numbers). This creates a symbol with *key* = *string* and *contents* = {*value, offset*).

“rfref *string* *number*” defines three symbols, with keys that are concatenations of “a”, “b and “w” with the *string*, The contents’ values are *number*, and their offsets are aoff, boff, and woff respectively. These constants must be defined as fields before the *rfref*. For the TC, aoff = 22, boff = 10, and woff = 27. The resulting symbols are used in later instructions to name the register file’s a, b, and w instruction fields for register *number*.

“mem string (which must evaluate to a number)” or “mem *number*” makes M[number] the current memory. Usually the “mem string” form will be used, after a preceding *field* definition of the memory name. Memories are numbered 0 to 2. Token processing continues after the string or number.

“loc *string* (which must evaluate to a number)” or “loc *number*” sets the current location in the current memory.

“string:” Provides a way to label statements. A symbol is defined with *key* = *string*, *contents* = {currentLocation, 10}. This is an Rb constant. Execution of a “Jump loc ” instruction substitutes this (12 bit) constant for RF[Rb] as the “b” input to the ALU, providing that location “loc” has been labeled. The default value for Ra is 0, and the default instruction is “add”, so the right thing happens.

It is also possible to do PC- relative Jumps: If Ra = 31, PC is used as the “a” input to the ALU instead of RF[Ra]. If both “*rfref* PC, 31” and “*rfref* Offset *displacement*” have been previously encountered in the source, then “Jump aPC + bOffset Jumps to PC + *displacement.*

When token processing for a line is finished, if any field in currentValue has been set, the value is placed into the current location in the current memory, and the current location in that memory is incremented. currentValue is cleared and scanning resumes at the start of the next line.

Because the assembler syntax is so loose, it is easy to write programs that don’t work. The usual error is to use the incorrect port variant for named registers. This would be worrisome, but we don’t expect to write very much assembly code for the machine. To make it a more useful teaching tool, a better assembler would be needed. A simulator would also be useful to try out programs before committing them to the hardware.

The example below shows a very small program, with the machine definition preceding the source code:

field aoff 22 0; define the field offsets for rfref. These names must be defined and must not change.

field boff 10 0;

field woff 27 0;

field instruction 0 0; name for instruction memmory

field rf 1 0; name for register file

field data 2 0; name for data memory

field := 0 0; noise word

field PC 0 0; noise word

field + 0 7; the “plus” function

field - 1 7; the “minus” function

field ++ 2 7; the “Rb + 1” function

field -- 3 7; the “Rb – 1” function

field & 4 7; the “and” function

field | 5 7; the “or” function

field ^ 6 7; the “xor” function

field &~ 7 7; the "and not" function

field rcy1 1 5;

field rcy8 2 5;

field rcy16 3 5;

field skn 1 3; skip if ALU < 0

field skz 2 3; skip if ALU = 0

field ski 3 3; skip if InRdy

field Store 1 0; Opcodes

field StoreI 2 0;

field Out 3 0;

field Load 4 0;

field In 5 0;

field Jump 6 0;

field Const 7 0;

mem instruction loc 1; Set current memory to the instruction memory, location 1.

rfref Trash 0; r0 used for both the trashcan and the source of zero

rfref Zero 0;

rfref Link 1; subroutine linkage register

;A test program to increment in the LEDs at ~600ms intervals.

rfref DelayCount 2; count this register down to delay

rfref OutValue 3; register to hold the value displayed in the LEDs

start: wDelayCount := Const 0x4;

Jump delay wLink := PC;

Out := wOutValue := bOutValue ++;

Jump start;

delay: wDelayCount := bDelayCount -- skz; subroutine to delay 40ns \* DelayCount

Jump delay;

Jump aLink; return

end